Web Images Videos Maps News Shopping Gmail more ▾　　　　　Sign in

Google scholar [transferring data between flash memory memc] Search Advanced Scholar Search
　　　　　　　　　　　　　　　　　　　　　　　　　　　Scholar Preferences

Scholar All articles Recent articles　　　Results **1 - 10** of about **61,200**. (0.26 sec)

The stanford **FLASH** multiprocessor- ▶ psu.edu [PDF]
J Kuskin, D Ofelt, M Heinrich, J Heinlein, R ... - International Symposium on Computer Architecture, 1998 -
portal.acm.org
... the avoidance of extra copying of message **data**. ... low-level messages MAGIC sends **between**
nodes, this ... consists of three phases: initiation, **transfer**, and reception ...
Cited by 762 - Related articles - BL Direct - All 28 versions

eNVy: a non-volatile, main **memory** storage system
M Wu, W Zwaenepoel - Proceedings of the sixth international conference on ..., 1994 - portal.acm.org
... done to overcome the deficiencies associated with **Flash** ... times, creating a discrepancy
between computation time ... available I/0 rates by **transferring data** in par- ...
Cited by 211 - Related articles - BL Direct - All 7 versions

... **memory** cards using **flash** EEPROM integrated circuit chips and **memory-controller** ...
RF Wallace, RD Norman, E Harari - US Patent 7,355,874, 2008 - Google Patents
... 23, 2006 Related US Application **Data** (60) Division ... with a large number of **flash** EEPROM
integrated ... common **controller** circuit that interfaces **between** the **memory** ...
Cited by 42 - Related articles - All 4 versions

... address counter for synchronously **transferring data between data** ports and buffer
memory
MC Stolowitz - US Patent 6,018,778, 2000 - Google Patents
... ARRAY **CONTROLLER** FOR READING/WRITING STRIPED **DATA** USING A SINGLE ADDRESS COUNTER
FOR SYNCHRONOUSLY **TRANSFERRING DATA BETWEEN DATA** PORTS AND BUFFER **MEMORY** [75 ...
Cited by 20 - Related articles - All 2 versions

Integration of message passing and shared **memory** in the Stanford **FLASH** multiprocessor-
▶ psu.edu [PDF]
J Heinlein, K Gharachorloo, S Dresser, A ... - Proceedings of the sixth international conference on ..., 1994 -
portal.acm.org
... can be further enhanced by avoiding message copying through direct **transfer** of **data**
between user address ... interaction of message **data** with cache coherence. ...
Cited by 101 - Related articles - BL Direct - All 10 versions

Magnetoresistive random access **memory** using magnetic tunnel junctions
S Tehrani, JM Slaughter, M Deherrera, BN .. - Proceedings of the IEEE, 2003 - ieeexplore.ieee.org
... circuit such as a processor or **controller**. ... by eliminating the need for **transferring**
data between multiple memories ... The capability of **Flash memory** to store **data** ...
Cited by 139 - Related articles - BL Direct - All 3 versions

A **flash-memory** based file system- ▶ smallworks.com [PDF]
A Kawaguchi, S Nishioka, H Motoda - usenix.org
... because the concurrent write operations can provide a higher **transfer** rate ... SRAM for
write buffering, a large-bandwidth parallel **data** path **between** them, and ...
Cited by 213 - Related articles - All 20 versions

System architecture directions for networked sensors- ▶ ucla.edu [PDF]

J Hill, R Szewczyk, A Woo, S Hollar, D Culler, ... - ACM Sigplan Notices, 2000 - portal.acm.org
... will want to explore the tradeoffs **between** the scale ... event is used to periodically
start the **data** collection. ... s send message com- mand to initiate a **transfer**. ...
Cited by 2636 - Related articles - BL Direct - All 128 versions

Tempest and Typhoon: User-level shared **memory**- ▶ wisc.edu [PDF]
SK Reinhardt, JR Larus, DA Wood - Proceedings of the 21ST annual international ..., 1994 - portal.acm.org
... both reads and writes, and **transfer** control to user ... **memory** involves replicat- ing
remote **data** to ensure ... are local and maintaining coherence **between** the multiple ...
Cited by 415 - Related articles - BL Direct - All 31 versions

The performance impact of flexibility in the Stanford **FLASH** multiprocessor- ▶ kfupm.edu.sa
[PDF]
M Heinrich, J Kuskin, D Ofelt, J Heinlein, J ... - Proceedings of the sixth international conference on ..., 1994 -
portal.acm.org
... stage **data** as it is passed **between** interfaces. ... bus, **memory** system, and network) and
data transfer delays ... the same cache-coherence protocol as **FLASH** (see Section ...
Cited by 142 - Related articles - BL Direct - All 18 versions

Goooooooooogle ▶

Result Page: 1 2 3 4 5 6 7 8 9 10   **Next**

[transferring data between flash mem] Search

Go to Google Home - About Google - About Google Scholar

©2009 Google